

FIG. 1

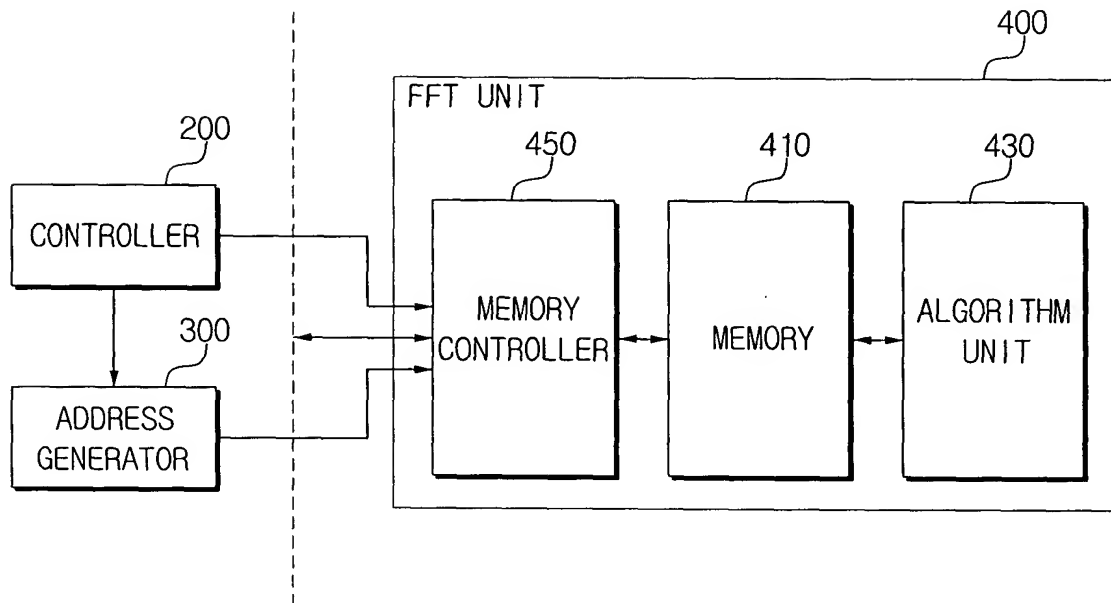


FIG.2A

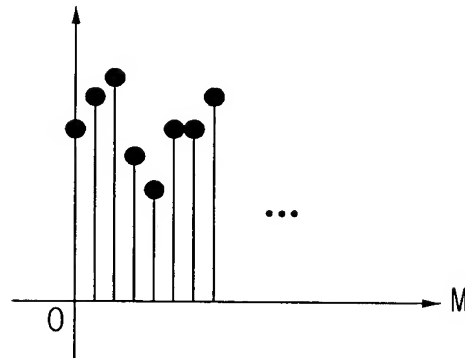


FIG.2B

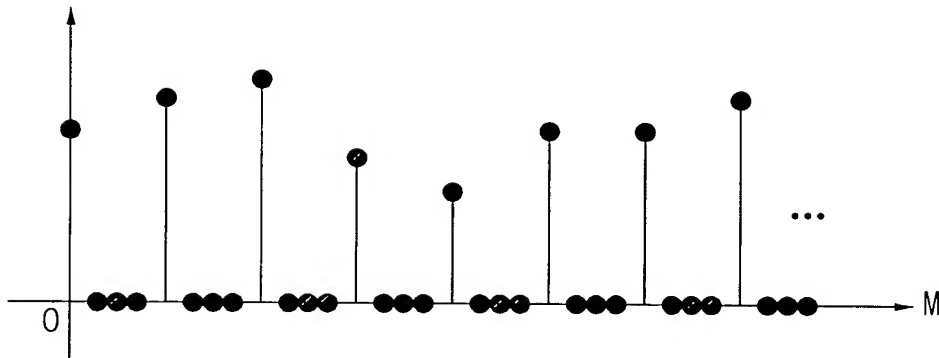


FIG.2C

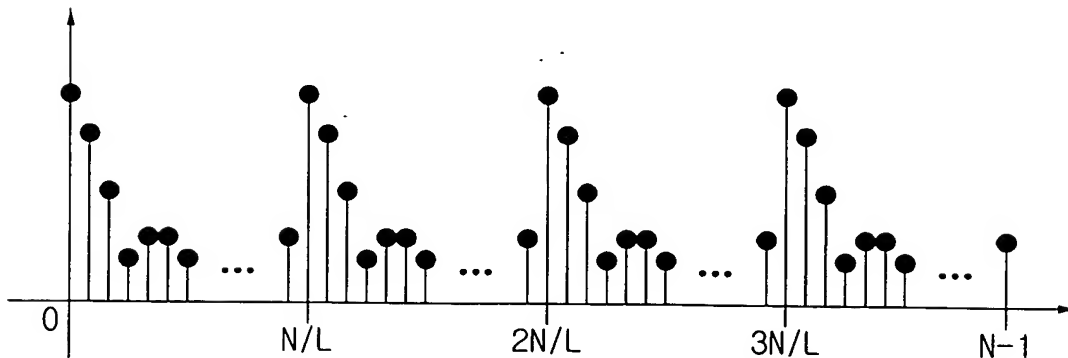


FIG.3

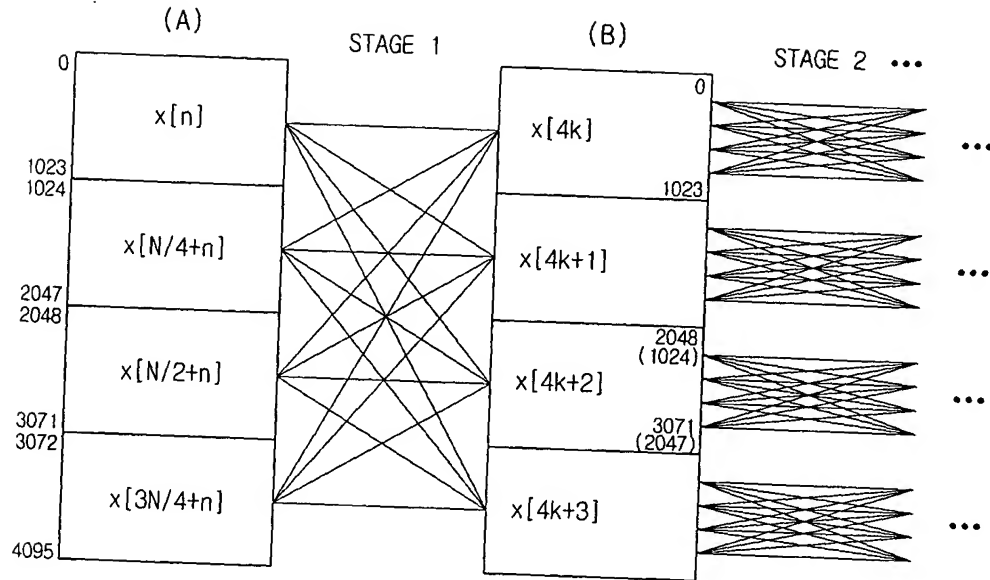


FIG.4

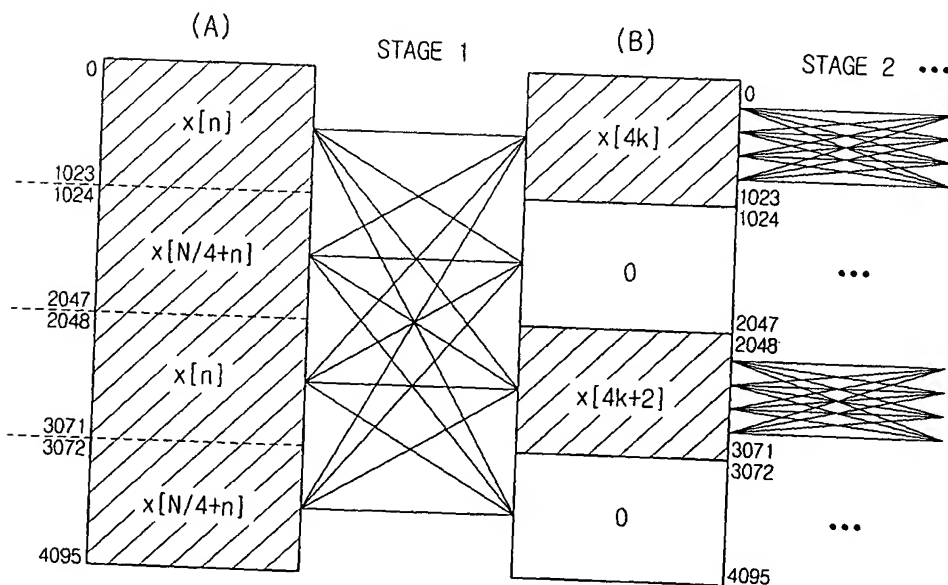


FIG.5

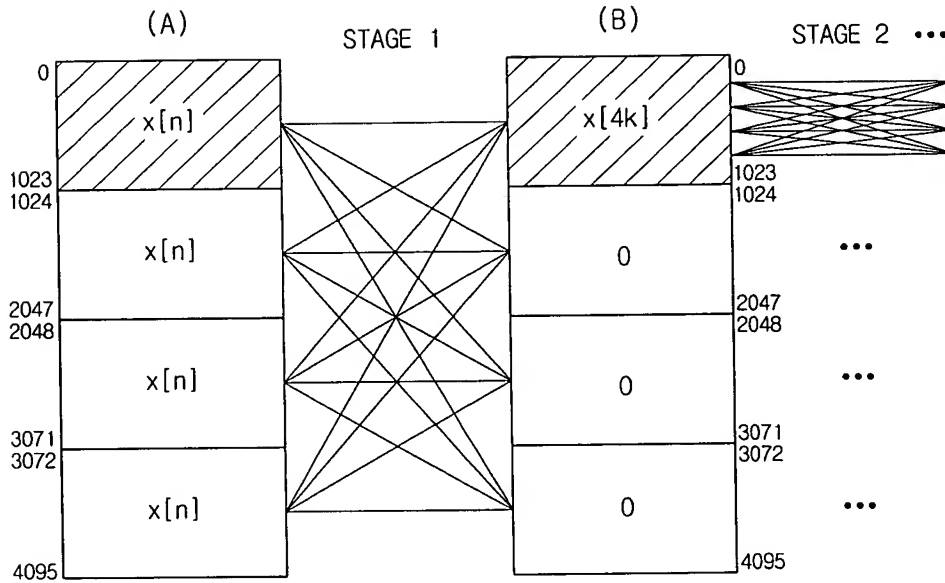


FIG.6

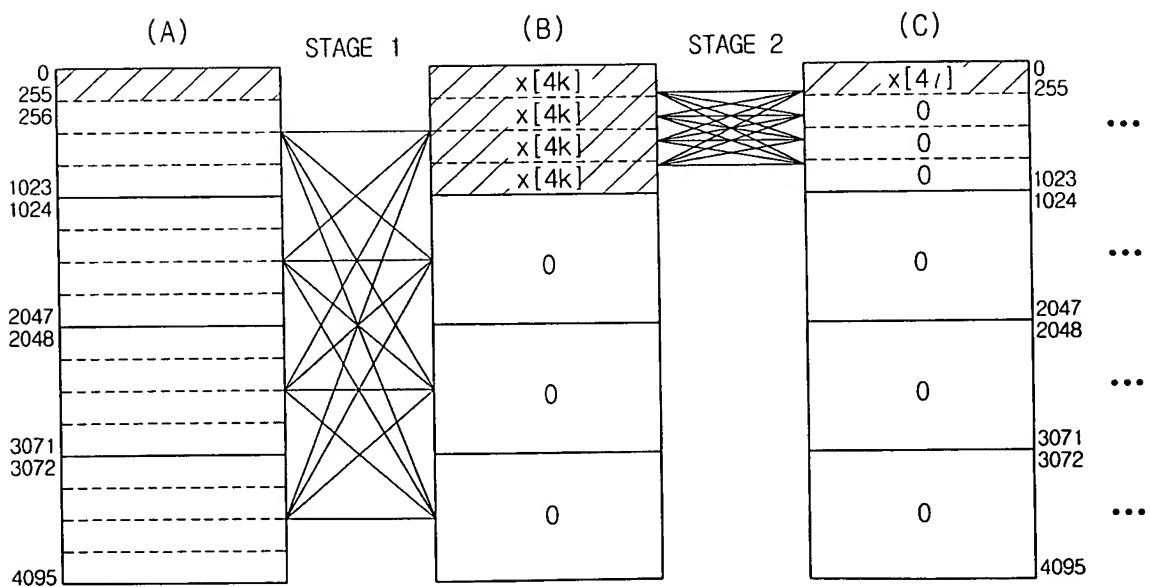


FIG. 7

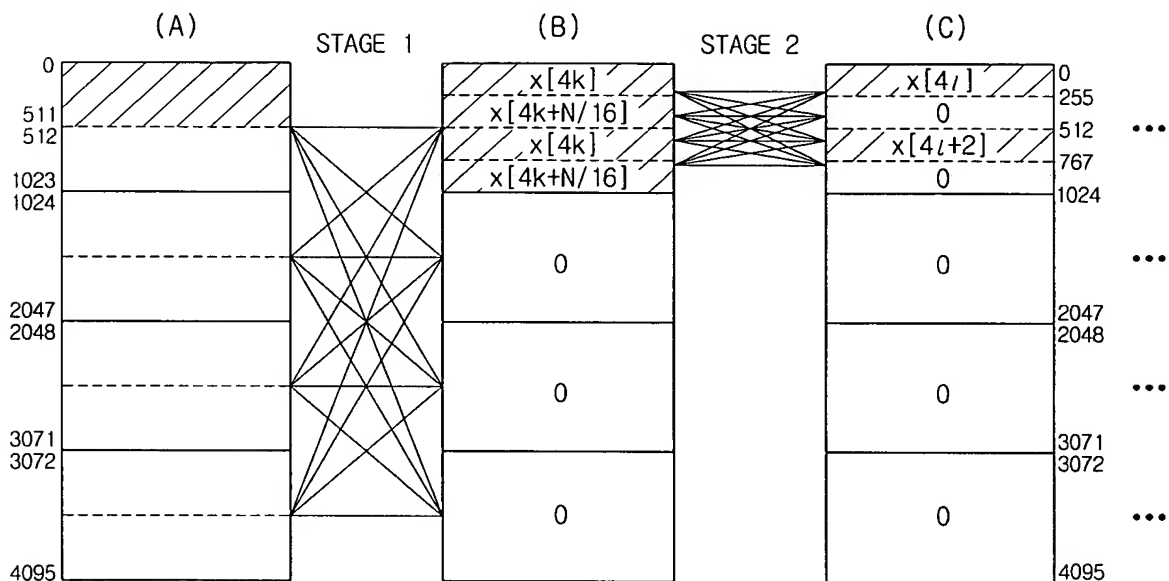


FIG. 8

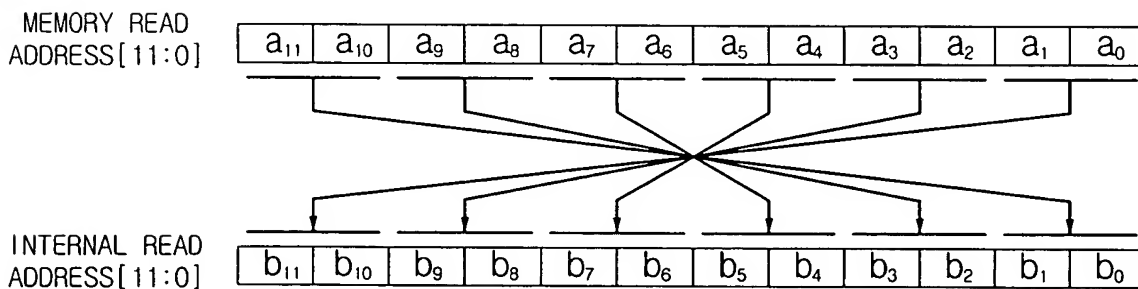


FIG.9A

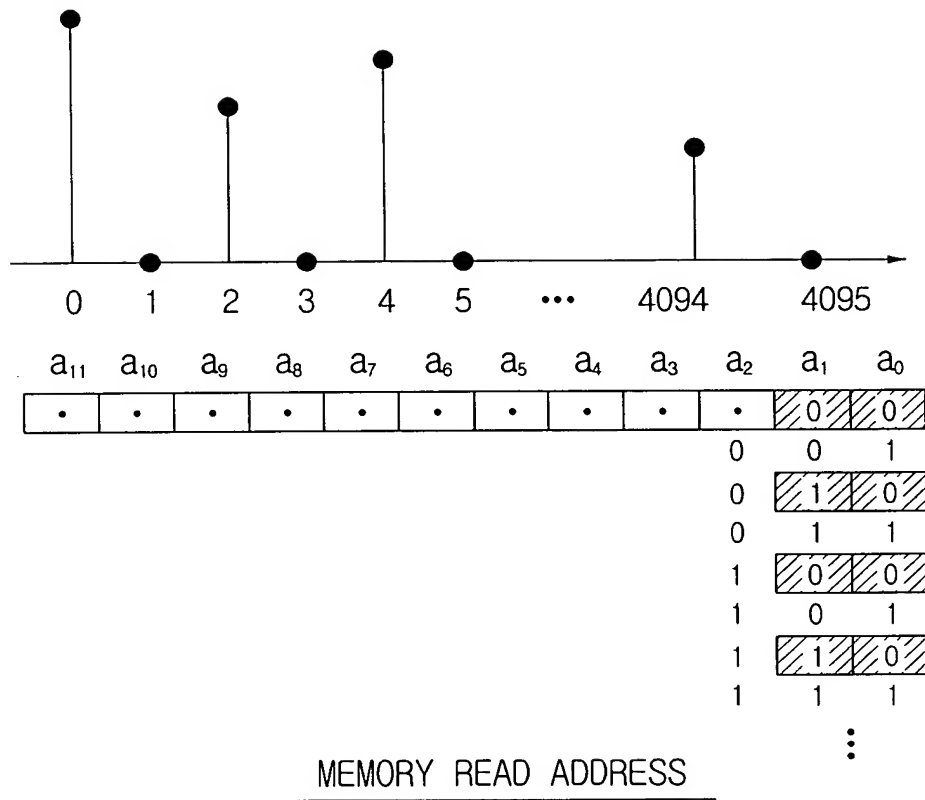


FIG.9B

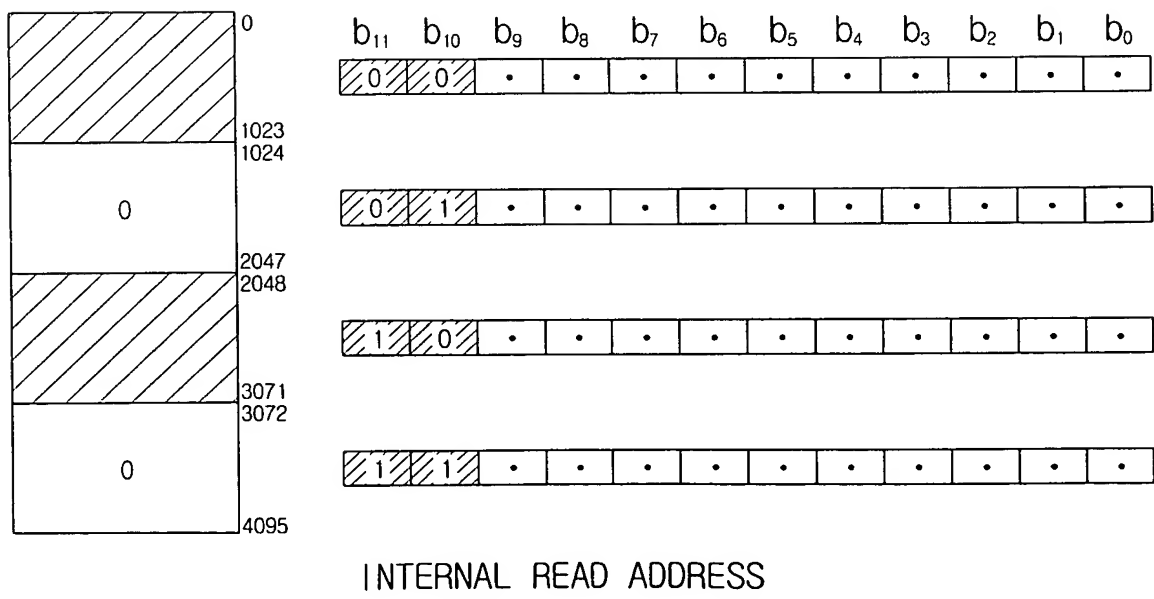


FIG.9C

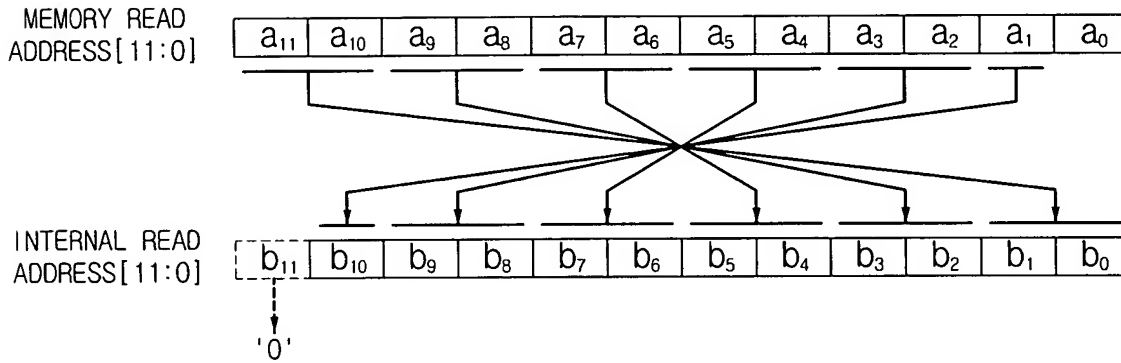
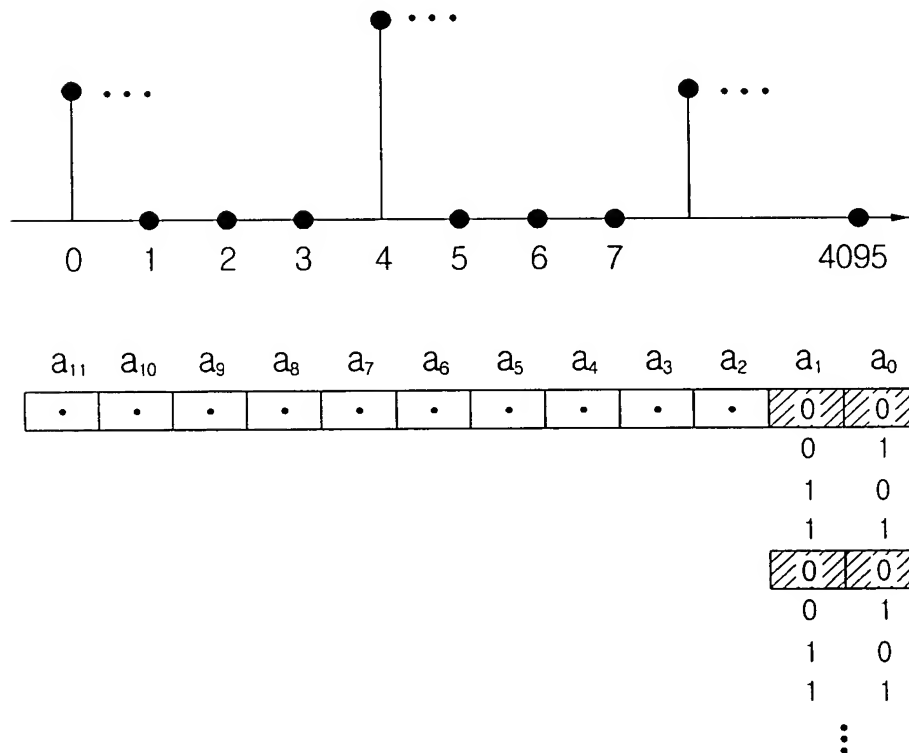


FIG.10A



MEMORY READ ADDRESS

FIG.10B

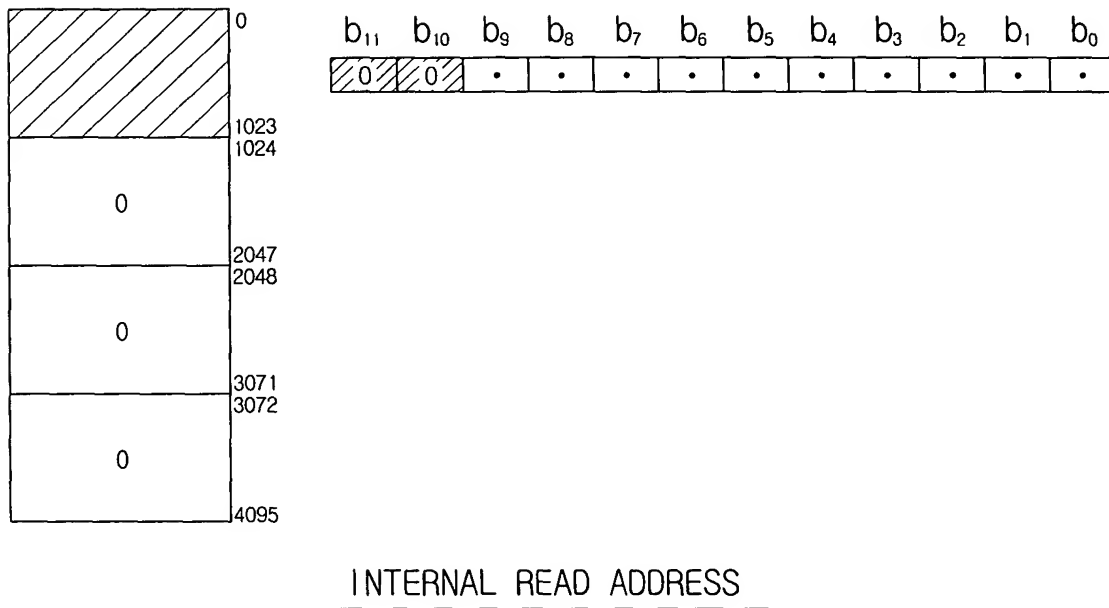


FIG.10C

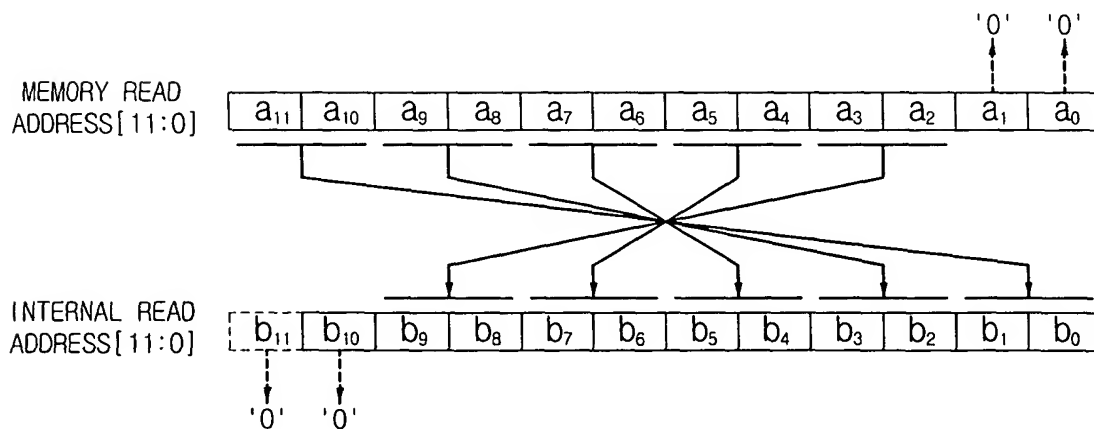




FIG. 11A

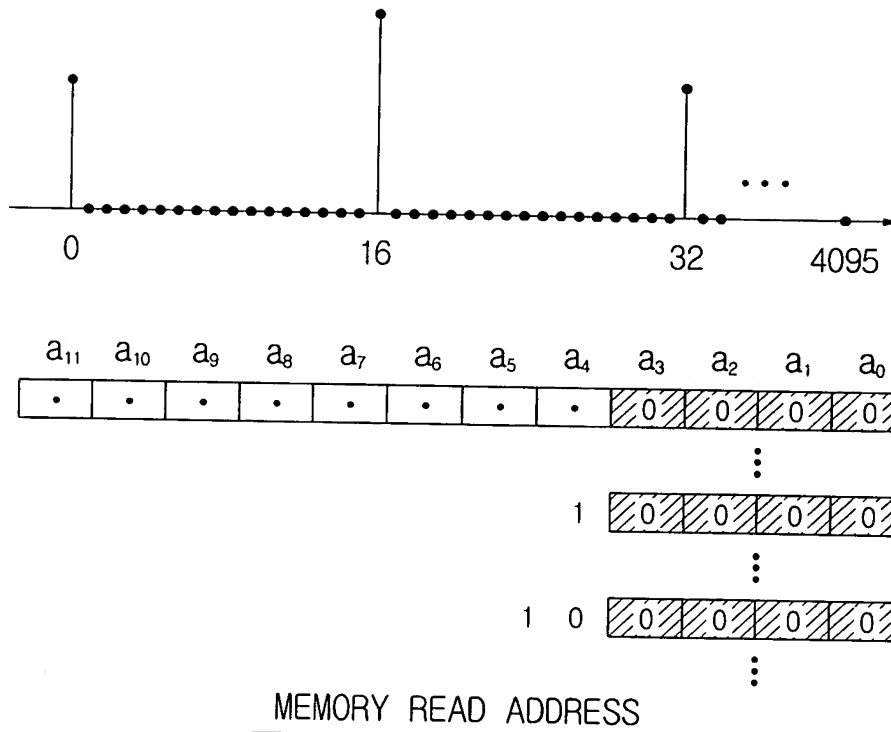


FIG. 11B

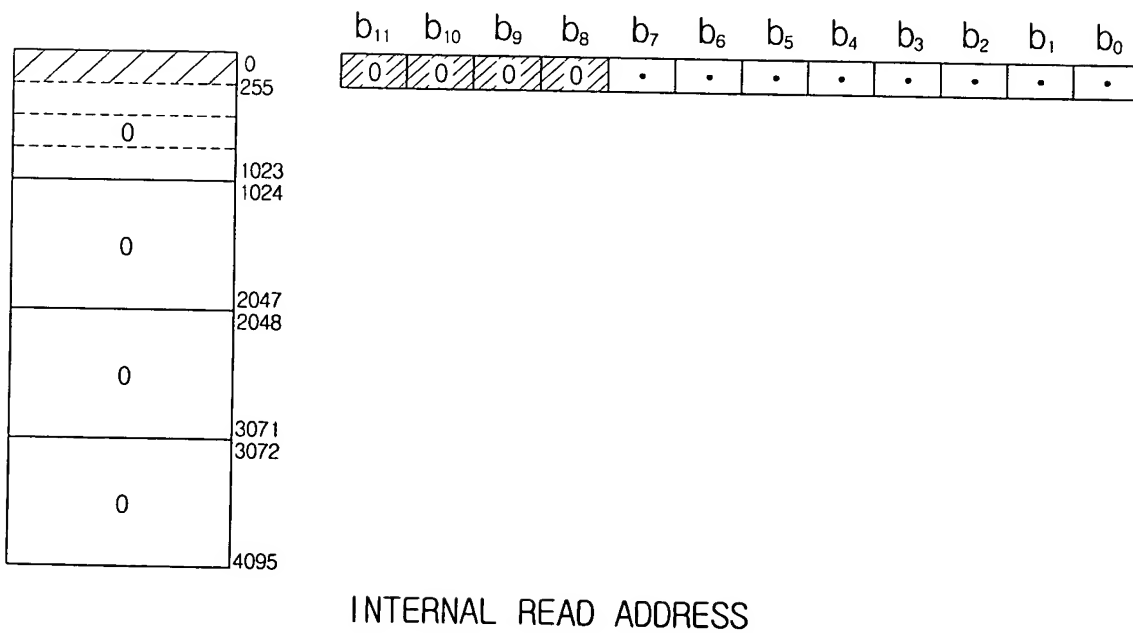


FIG. 11C

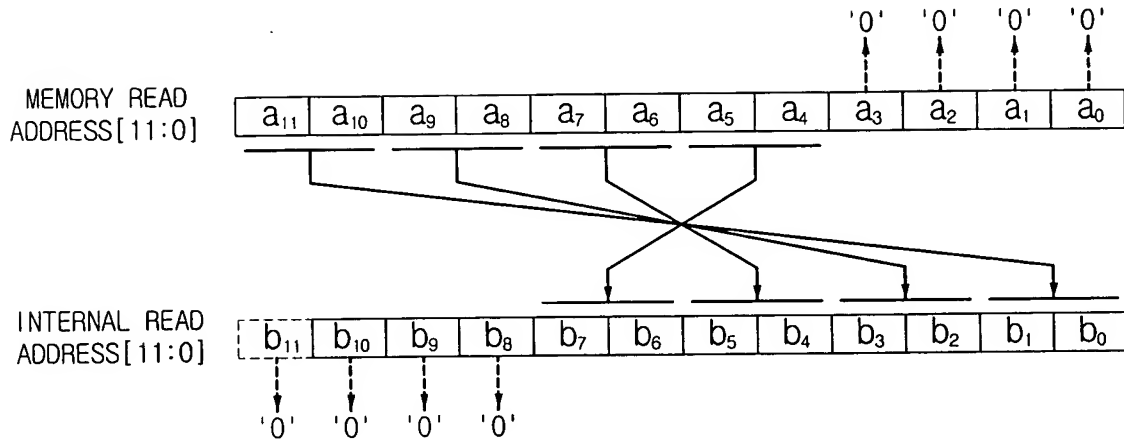


FIG. 12A

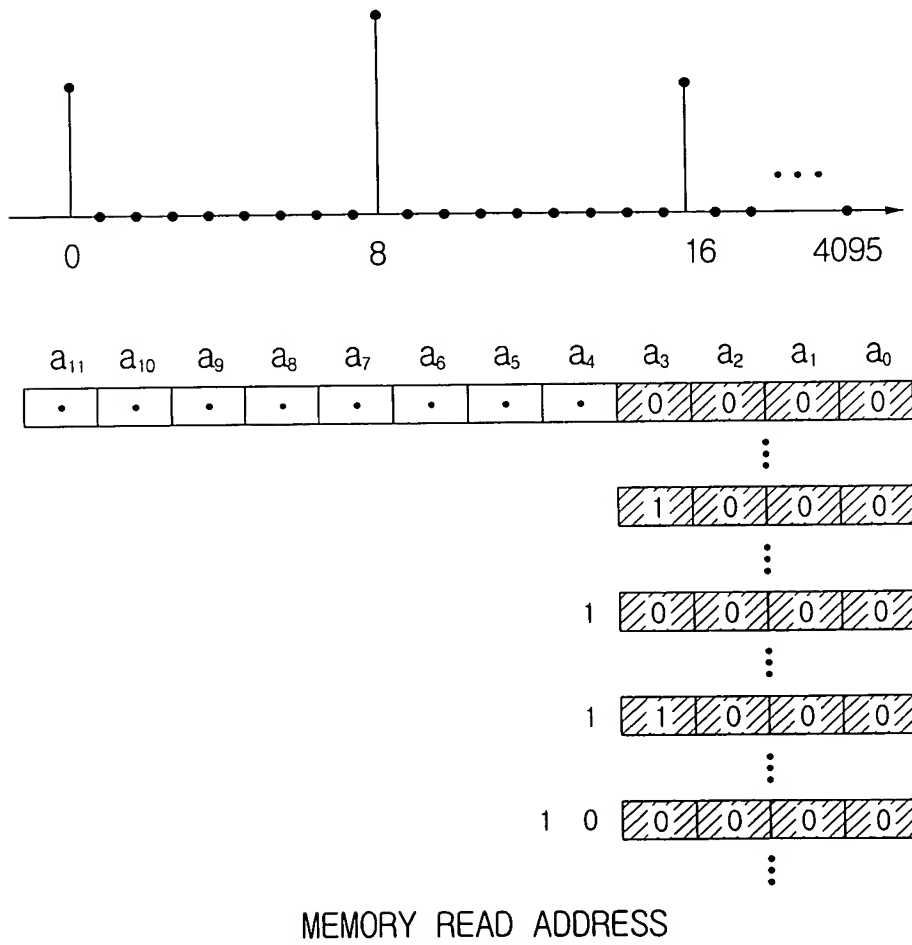


FIG.12B

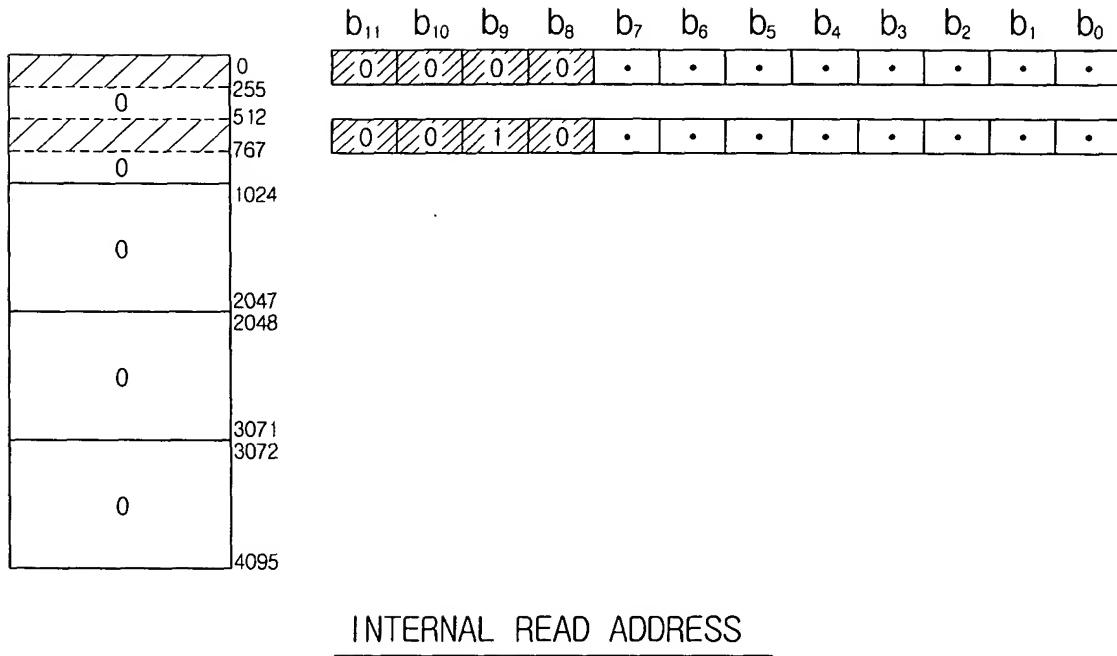


FIG.12C

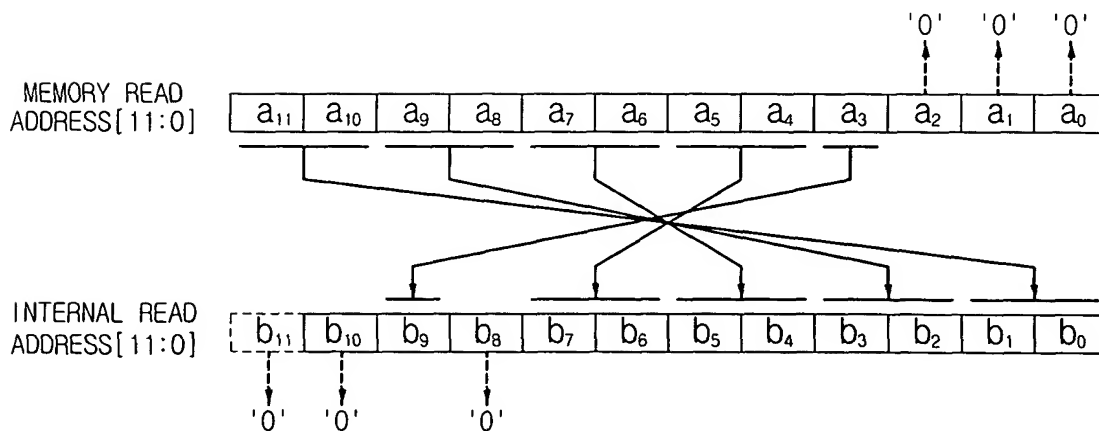


FIG.13

